

WHAT IS CLAIMED IS:

1. A method comprising:
 - setting an indicator in a buffer;
 - reading pixel data for a current video line from the buffer;
 - determining when the pixel data reaches the indicator;
 - and
 - loading data for the next video line into the buffer.
2. The method of Claim 1, further comprising setting the indicator at approximately a middle of the buffer.
3. The method of Claim 1, further comprising loading data for the next video line to replace data for the current video line in the buffer.
4. The method of Claim 1, further comprising processing the current video line data for display.
5. The method of Claim 4, further comprising displaying the processed video line data.
6. The method of Claim 5, further comprising creating a video overlay from the processed video line data.

7. The method of Claim 1, further comprising
positioning the pixel data on an active display to create a
video overlay.

8. ~~A method of processing video overlay data~~
comprising:

reading video data for a current video line from a
buffer;

detecting the position in the buffer the video data is
located;

loading data for the next video line into the buffer when
the video data for the current video line is located at a
predetermined position.

9. The method of Claim 12, further comprising setting
the predetermined position at a position before all the
current video line data is read.

10. ~~The method of Claim 8, further comprising setting~~
the predetermined position at approximately a midpoint of the
buffer.

11. The method of Claim 8, further comprising loading
data for the next video line to replace data for the current
video line in the buffer.

12. The method of Claim 8, further comprising processing the current video line data for display.

13. The method of Claim 12, further comprising displaying the processed video line data.

14. A overlay display processor comprising:

a buffer having a plurality of memory locations, the buffer adapted to provide data to a display; and

an indicator positioned at a predetermined memory location in the buffer, wherein the buffer begins to read data for a next video data line when the buffer provides data from the indicator memory location.

15. The computer of Claim 14, further comprising graphic memory which provides the video pixel data to the buffer.

16. The computer of Claim 14, wherein the buffer provides data to the display for a current video line.

17. The computer of Claim 14, wherein the indicator is located at a position at approximately a midpoint of the buffer.

18. A overlay display system comprising:

video memory which stores video data;

3 ~~an overlay processing engine comprising:~~

4 a buffer which receives the video data from the
5 memory

6 video processing circuitry for preparing the video
7 data in the buffer to be displayed; and

8 a display which receives the processed data from the
9 overlay processing engine, wherein the buffer begins to read
10 data for a next video data line when the buffer provides a
11 predetermined amount of data to the display for a current
12 video data line.

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19. The computer of Claim 18, wherein the predetermined
2 amount of data is approximately half the data comprising the
3 current video data line.

1 20. The computer of Claim 18, wherein the overlay
2 processing engine provides data to the display to create a
3 video overlay.

1 21. The computer of Claim 18, wherein the video
2 processing circuitry includes pixel color conversion and
3 adjustment.

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1 ~~22. A program storage device readable by a machine~~
2 ~~comprising instructions that cause the machine to.~~

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~~approximately a middle of the buffer.~~